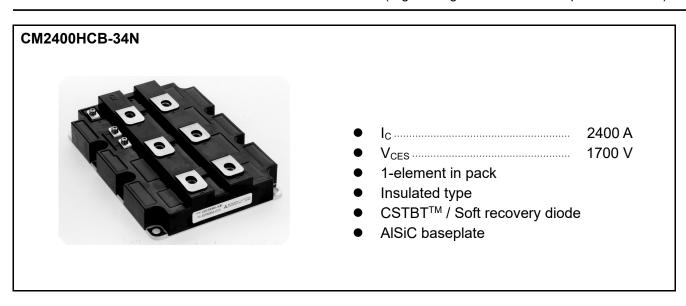


<High Voltage Insulated Gate Bipolar Transistor: HVIGBT >

CM2400HCB-34N

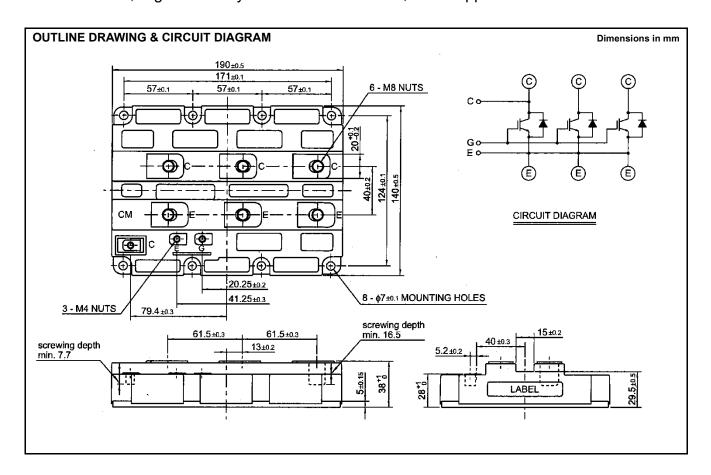
HIGH POWER SWITHCHING USE INSULATED TYPE

4th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules



APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers



HIGH POWER SWITHCHING USE

INSULATED TYPE 4th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

MAXIMUM RATINGS

Symbol	Item	Conditions	Ratings	Unit
V _{CES}	Collector-emitter voltage	V _{GE} = 0V, T _j = 25 °C	1700	V
V_{GES}	Gate-emitter voltage	$V_{CE} = 0V, T_j = 25 ^{\circ}C$	± 20	V
Ic	Collector current	DC, T _c = 80 °C	2400	Α
I _{CRM}	Collector current	Pulse (Note 1)	4800	Α
I _E	Emitter current (Note 2)	DC	2400	Α
I _{ERM}	Emilier current (****-5)	Pulse (Note 1)	4800	Α
P _{tot}	Maximum power dissipation (Note 3)	T _c = 25 °C, IGBT part	15600	W
V _{iso}	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1min.	4000	V
Tj	Junction temperature		-40 ~ +150	°C
T _{jop}	Operating temperature		-40 ~ +125	°C
T _{stg}	Storage temperature		-40 ~ +125	°C
t _{pSC}	Maximum short circuit pulse width	$V_{CC} = 1000V, V_{CE} \le V_{CES}, V_{GE} = 15V, T_j = 125^{\circ}C$	10	μs

ELECTRICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
Symbol	item			Min	Тур	Max	Offic
1.	Collector cutoff current	V _{CE} = V _{CES} , V _{GE} = 0 V	T _j = 25 °C	_	_	9	mA
I _{CES}	Collector cutoff current	VCE - VCES, VGE - UV	T _j = 125 °C		7.0	18	ША
$V_{GE(th)}$	Gate-emitter threshold voltage	$V_{CE} = 10 \text{ V}, I_{C} = 240 \text{ mA}, T_{j} = 25 ^{\circ}\text{C}$		5.5	6.5	7.5	V
I _{GES}	Gate leakage current	$V_{GE} = V_{GES}, V_{CE} = 0V, T_{j} = 25^{\circ}C$		-0.5	_	0.5	μΑ
C _{ies}	Input capacitance	V _{CF} = 10 V. V _{GF} = 0 V. f = 100 kHz		_	396	_	nF
C _{oes}	Output capacitance	02 , 02 ,		_	21.6	_	nF
C _{res}	Reverse transfer capacitance	$T_j = 25 ^{\circ}\text{C}$		_	6.3	_	nF
Q_G	Total gate charge	V_{CC} = 900 V, I_{C} = 2400 A, V_{GE} = ±15 V	,	_	27.4	_	μC
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	0 11 11 11 11	I _C = 2400 A (Note 4)	T _j = 25 °C	_	2.10	2.70	V
V _{CEsat}	Collector-emitter saturation voltage	.0 =	T _j = 125 °C	_	2.35	_	
t _{d(on)}	Turn-on delay time	V _{CC} = 900 V, I _C = 2400 A		_	0.90	_	μs
t _r	Turn-on rise time	V_{GE} = ±15 V, $R_{G(on)}$ = 0.8 Ω T_{j} = 125 °C, L_{s} = 80 nH Inductive load		_	0.30	_	μs
E _{on(10%)}	Turn-on switching energy (Note 5)				0.83	_	J
$t_{d(off)}$	Turn-off delay time	V _{CC} = 900 V, I _C = 2400 A		1	1.60	_	μs
t _f	Turn-off fall time	V_{GE} = ±15 V, $R_{G(off)}$ = 1.1 Ω T_j = 125 °C, L_s = 80 nH Inductive load		_	0.25	_	μs
E _{off(10%)}	Turn-off switching energy (Note 5)			_	0.70	_	J
	Emitter collector voltage (Note 2)	I _E = 2400 A (Note 4)	T _j = 25 °C	_	2.20	3.00	V
V _{EC}	Emitter-collector voltage (Note 2)	V _{GE} = 0 V	T _j = 125 °C	_	1.85	_	V
t _{rr}	Reverse recovery time (Note 2)	V _{CC} = 900 V, I _E = 2400 A		_	0.90	_	μs
Q _{rr}	Reverse recovery charge (Note 2)	V_{GE} = ±15 V, $R_{G(on)}$ = 0.8 Ω T_j = 125 °C, L_s = 80 nH Inductive load			750		μC
E _{rec(10%)}	Reverse recovery energy (Note 2) (Note 5)			_	0.42	_	J

THERMAL CHARACTERISTICS

THE MINE STATES								
Symbol	Item	Conditions	Limits			Linit		
			Min	Тур	Max	Unit		
$R_{th(j-c)Q}$	Thermal resistance	Junction to Case, IGBT part		_	8.0	K/kW		
$R_{th(j-c)D}$		Junction to Case, FWDi part		_	12.0	K/kW		
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, $\lambda_{grease} = 1W/m^*k$, $D_{(c-s)} = 100\mu m$	_	6.0	_	K/kW		

< High Voltage Insulated Gate Bipolar Transistor: HVIGBT >

CM2400HCB-34N

HIGH POWER SWITHCHING USE INSULATED TYPE

4th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			1.1
			Min	Тур	Max	Unit
M_t	Mounting torque	M8 : Main terminals screw	7.0	_	13.0	N·m
Ms		M6 : Mounting screw	3.0	_	6.0	N·m
Mt		M4 : Auxiliary terminals screw	1.0	_	2.0	N·m
m	Mass		I	1.5		kg
CTI	Comparative tracking index		600	_	_	_
da	Clearance		19.5	_	_	mm
d _s	Creepage distance		32.0	_		mm
L _{P CE}	Parasitic stray inductance		I	10.0		nΗ
R _{CC'+EE'}	Internal lead resistance	T _C = 25 °C	_	0.18	_	mΩ

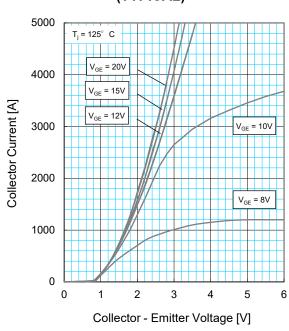
Note1. Pulse width and repetition rate should be such that junction temperature (Tj) does not exceed Tjopmax rating.

- 2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD_i).
- 3. Junction temperature (T_j) should not exceed T_{jmax} rating (150°C).
- 4. Pulse width and repetition rate should be such as to cause negligible temperature rise.
- 5. $E_{on(10\%)}$ / $E_{off(10\%)}$ / $E_{rec(10\%)}$ are the integral of 0.1 V_{CE} x 0.1 I_C x dt.

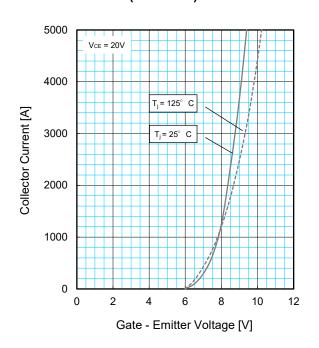
PERFORMANCE CURVES

INSULATED TYPE

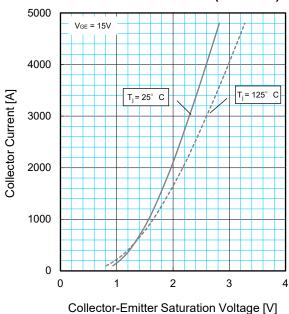
OUTPUT CHARACTERISTICS (TYPICAL)



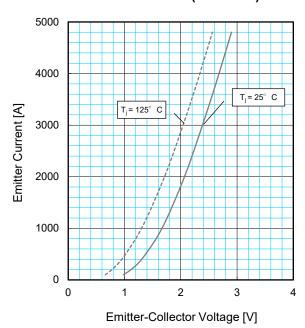
TRANSFER CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



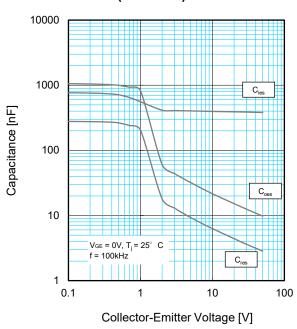
FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



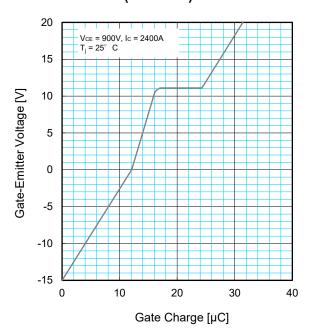
PERFORMANCE CURVES

INSULATED TYPE

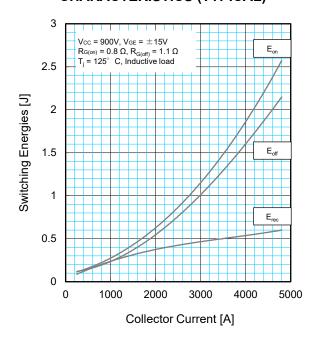
CAPACITANCE CHARACTERISTICS (TYPICAL)



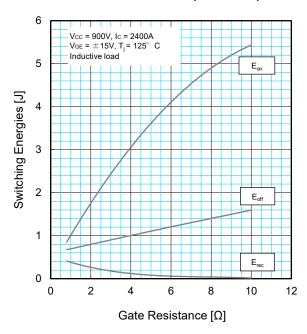
GATE CHARGE CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

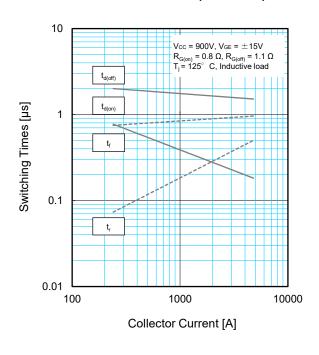


HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

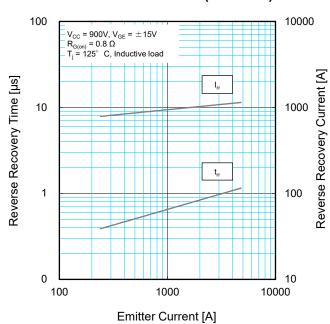


INSULATED TYPE

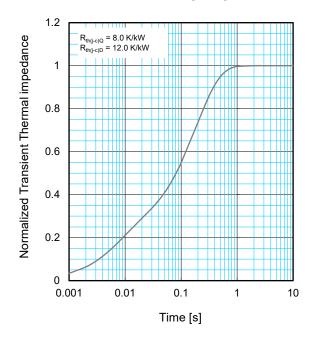
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS

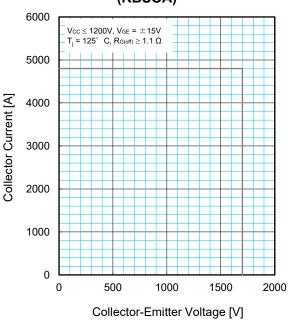


$$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_{i} \left\{ 1 - exp^{\left(-\frac{t}{\tau_{i}}\right)} \right\}$$

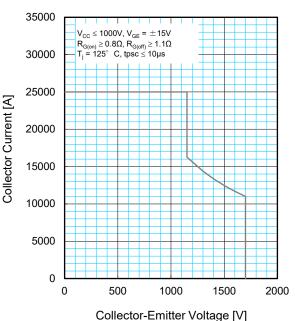
PERFORMANCE CURVES

INSULATED TYPE

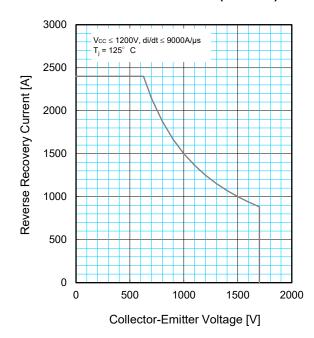
REVERSE BIAS SAFE OPERATING AREA (RBSOA)



SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)



FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)



4th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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