

2SK3924-01L,S,SJ

N-CHANNEL SILICON POWER MOSFET

■ Outline Drawings (mm) 200406

FUJI POWER MOSFET Super FAP-G Series

■ Features

- High speed switching
- Low on-resistance
- No secondary breakdown
- Low driving power
- Avalanche-proof

■ Applications

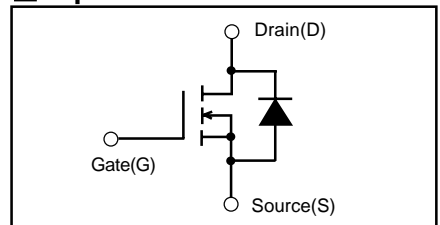
- Switching regulators
- DC-DC converters
- UPS (Uninterruptible Power Supply)

■ Maximum ratings and characteristic

● Absolute maximum ratings (Tc=25°C unless otherwise specified)

Item	Symbol	Ratings	Unit	Remarks
Drain-source voltage	V _{DS}	250	V	
	V _{D SX}	220	V	V _{GS} =-30V
Continuous Drain Current	I _D	14	A	
Pulsed Drain Current	I _{D(puls)}	±56	A	
Gate-Source Voltage	V _{GS}	±30	V	
Maximum Avalanche current	I _{AR}	14	A	Note *1
Non-Repetitive Maximum Avalanche Energy	E _{AS}	301.1	mJ	Note *2
Repetitive Maximum Avalanche Energy	E _{AR}	10.5	mJ	Note *3
Maximum Drain-Source dV/dt	dV _{DS} /dt	20	kV/μs	V _{DS} ≤250V
Peak Diode Recovery dV/dt	dV/dt	5	kV/μs	Note *4
Peak Diode Recovery -di/dt	-di/dt	100	A/μs	Note *5
Max. Power Dissipation	P _D	105	W	T _c =25°C
		1.67		T _a =25°C
Operating and Storage Temperature range	T _{ch}	+150	°C	
	T _{stg}	-55 to +150	°C	

■ Equivalent circuit schematic



Note *1: T_{ch} ≤ 150°C, Repetitive and Non-repetitive

Note *2: Starting T_{ch}=25°C, I_{AS}=6A, L=14.1mH,

V_{CC}=48V, R_G=50Ω

E_{AS} limited by maximum channel temperature and Avalanche current.

See to the 'Avalanche Energy' graph

Note *3: Repetitive rating: Pulse width limited by maximum channel temperature.

See to the 'Transient Thermal impedance' graph.

Note *4: I_F ≤ -I_D, -di/dt=100A/μs, V_{CC} ≤ BV_{DSS}, T_{ch} ≤ 150°C

Note *5: I_F ≤ -I_D, dv/dt=5kV/μs, V_{CC} ≤ BV_{DSS}, T_{ch} ≤ 150°C

● Electrical characteristics (Tc = 25°C unless otherwise specified)

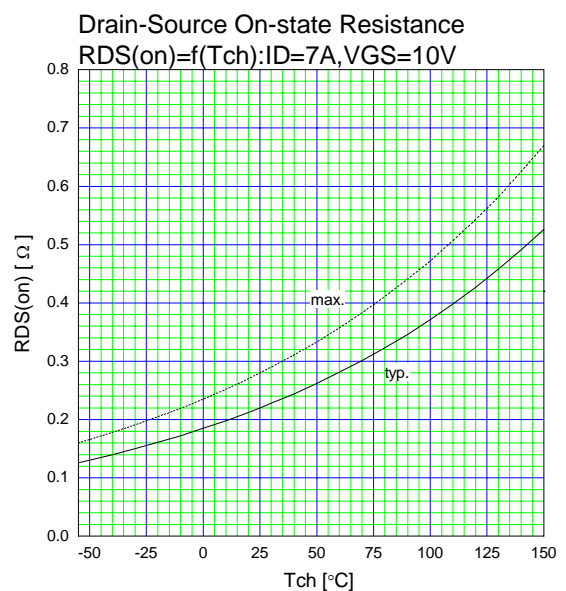
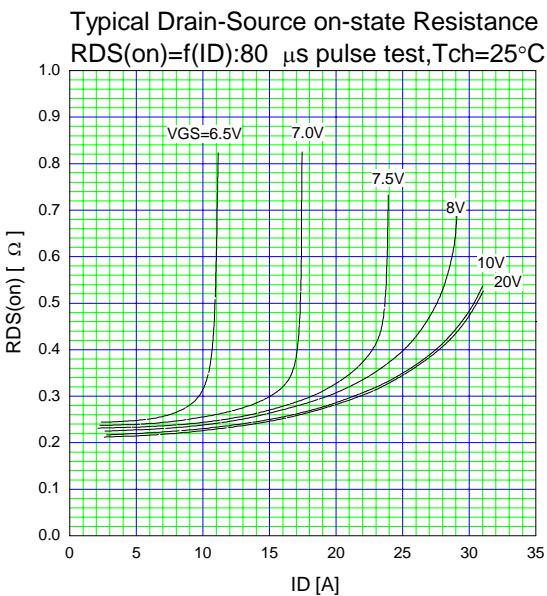
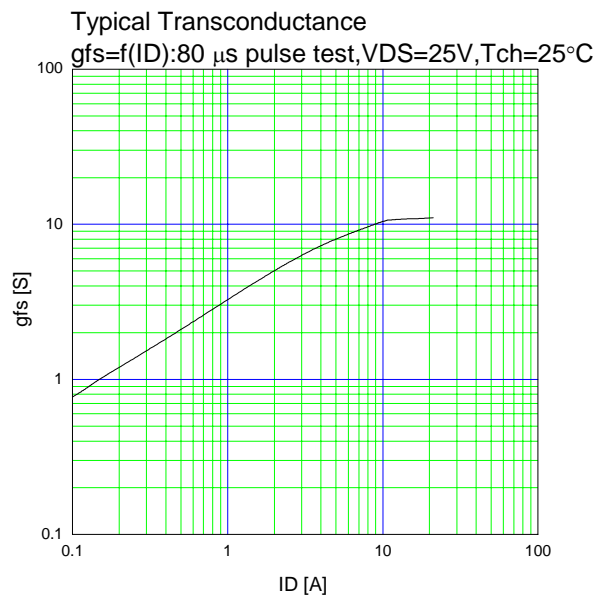
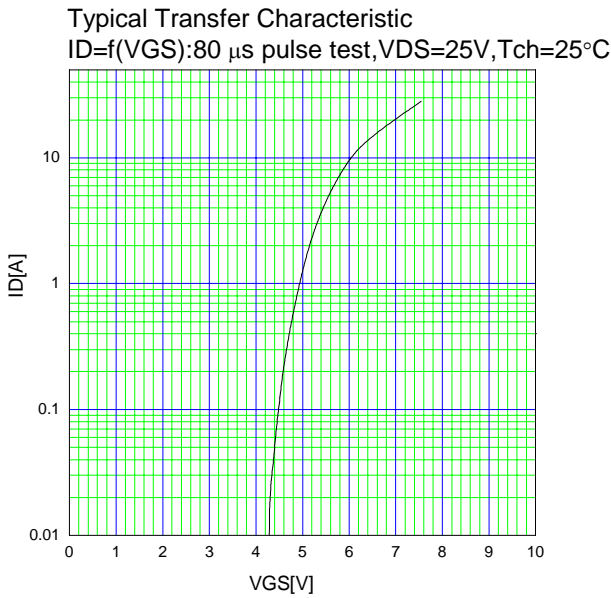
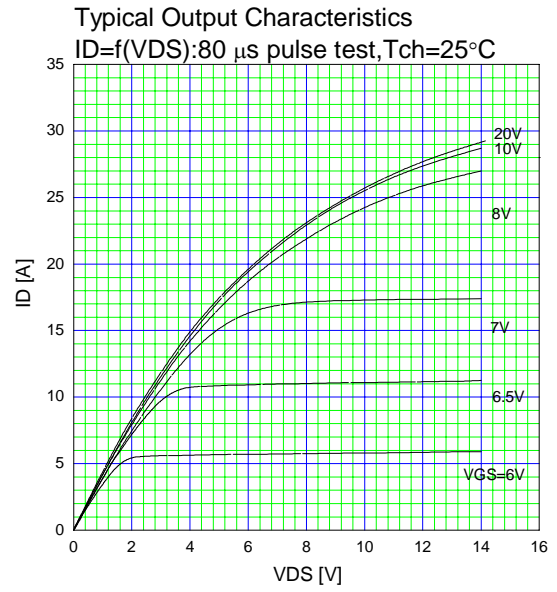
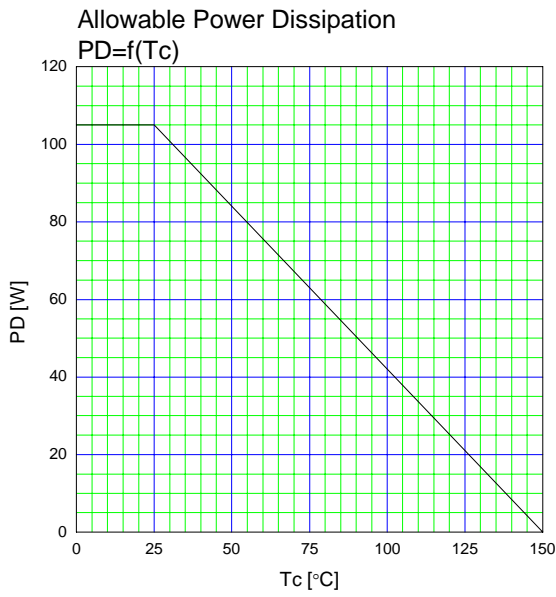
Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA V _{GS} =0V	250			V
Gate Threshold Voltage	V _{GS(th)}	I _D =250μA V _{DS} =V _{GS}	3.0		5.0	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =250V V _{GS} =0V			25	μA
		V _{DS} =200V V _{GS} =0V			2.0	mA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±30V V _{DS} =0V			100	nA
Drain-Source On-State Resistance	R _{DS(on)}	I _D =7A V _{GS} =10V		220	280	mΩ
Forward Transconductance	g _{fs}	I _D =7A V _{DS} =25V	5	10		S
Input Capacitance	C _{iss}	V _{DS} =75V		780	1170	pF
Output Capacitance	C _{oss}	V _{GS} =0V		90	135	
Reverse Transfer Capacitance	C _{rss}	f=1MHz		6.0	9.0	
Turn-On Time t _{on}	td(on)	V _{CC} =48V I _D =7A		12	18	ns
	t _r	V _{GS} =10V		3	4.5	
Turn-Off Time t _{off}	td(off)	R _{GS} =10Ω		23	35	
	t _f			6	9	
Total Gate Charge	Q _G	V _{CC} =125V		22	33	nC
Gate-Source Charge	Q _{GS}	I _D =14A		7.0	11	
Gate-Drain Charge	Q _{GD}	V _{GS} =10V		6.0	9.0	
Diode forward on-voltage	V _{SD}	I _F =14A V _{GS} =0V T _{ch} =25°C		1.00	1.50	V
Reverse recovery time	t _{rr}	I _F =14A V _{GS} =0V		120	250	ns
Reverse recovery charge	Q _{rr}	-di/dt=100A/μs T _{ch} =25°C		0.5	1.25	μC

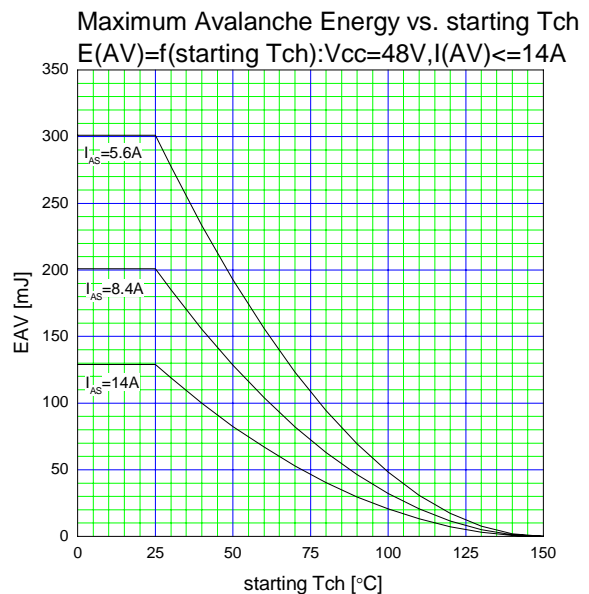
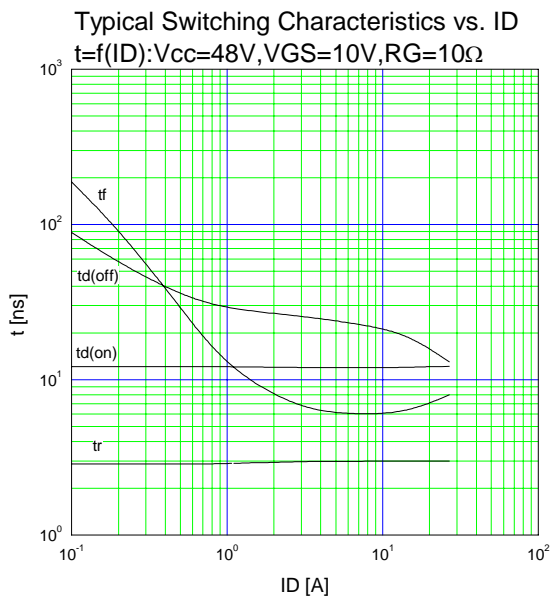
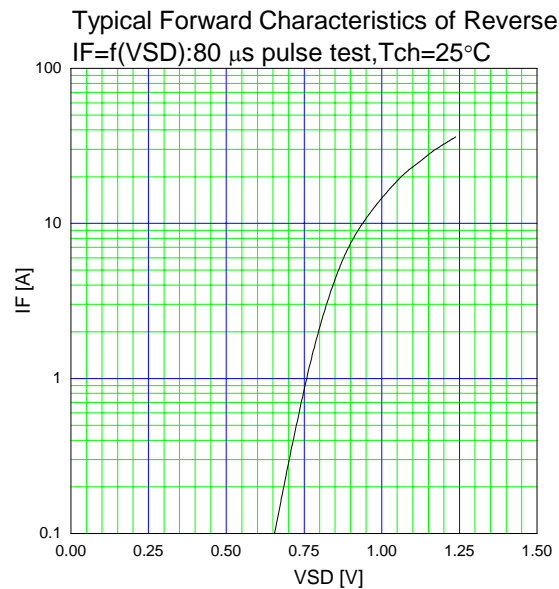
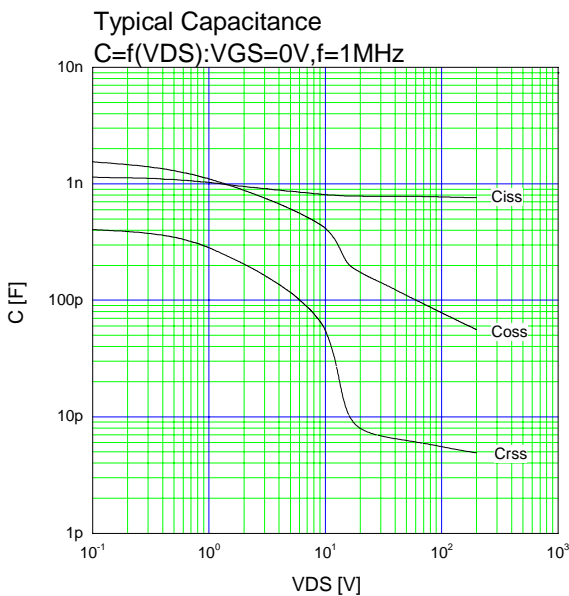
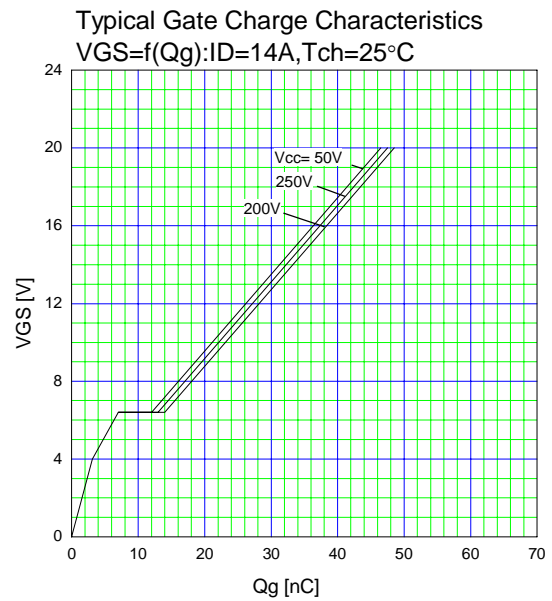
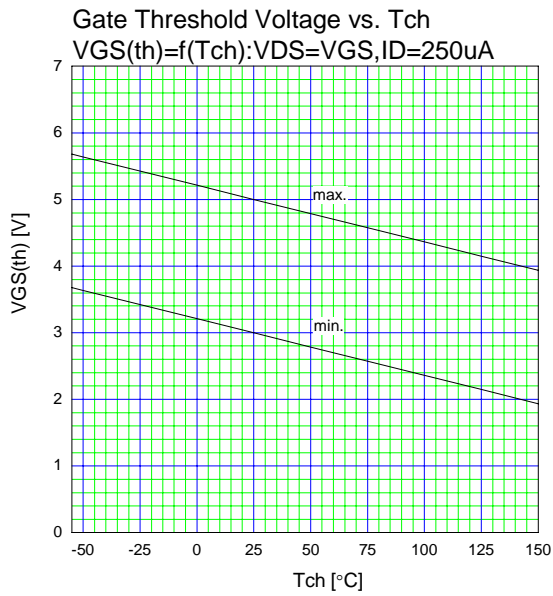
● Thermal characteristics

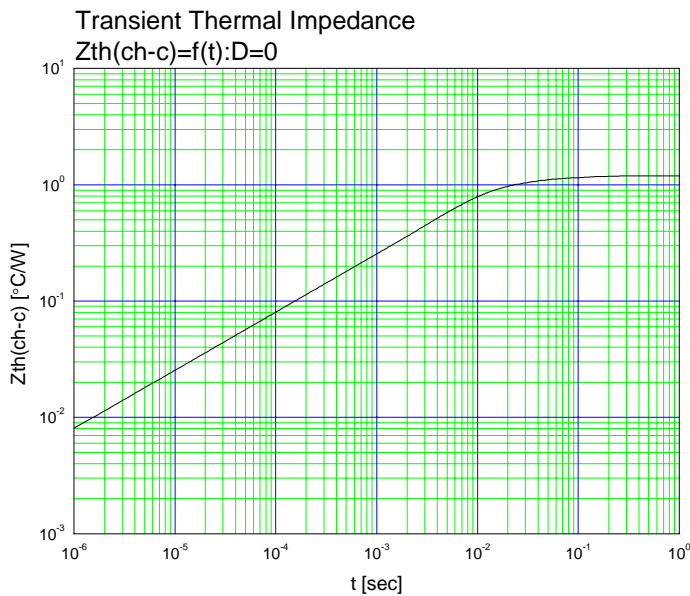
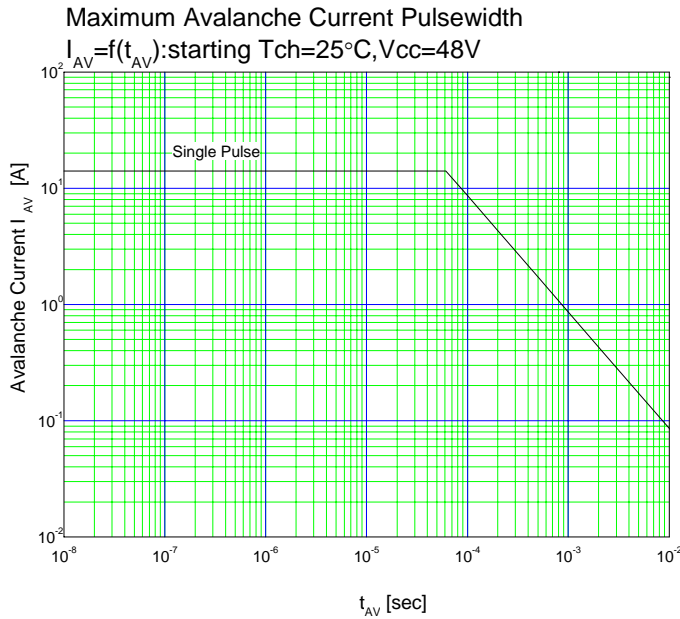
Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal resistance	R _{th(ch-c)}	channel to case			1.191	°C/W
	R _{th(ch-a)}	channel to ambient			75	°C/W

See to P4

Characteristics







Outline Drawings (mm)

